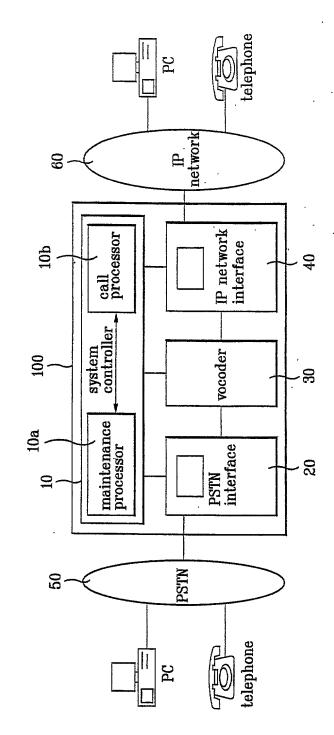
FIG. 1





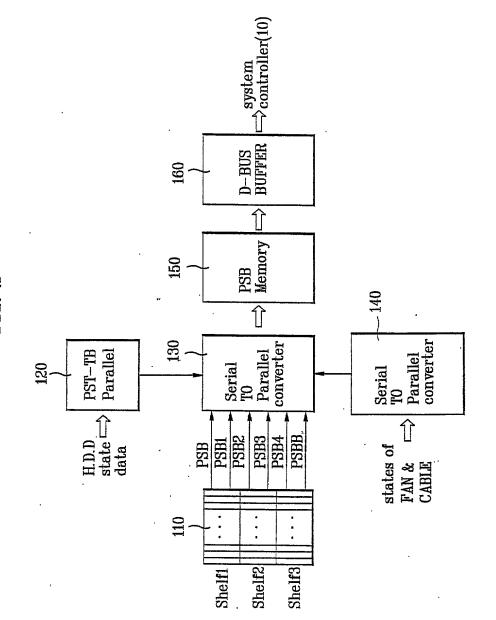


FIG. 3A

ī				
	Add	b7∼b4	b3~b0	Remarks
shelf-	0003	DPM1 LC2	DPM1 LC1	DKU,PSU: PSB_TB conversion from PSB-TB to PSB
		LC4	LC3	
		LC6	LC5	
		rc8	LC7	
		RDCC1	RDCC0	
			(HUBC)	
		DKU3,4	DKU1,2	
		PSU0,1	DKU5,6	
shelf-	000Ъ	DPM2 LC2	DPM2 LC1	
		LC4	LC3	,
		LC6	LC5	34
		LC8	LC7	
		RDCC1	RDCC0	
			(HUBC)	
		DKU3,4	DKU1,2	
		PSU0,1	DKU5,6	
	0010 0011 0012 0013 0014 0015 0016 0017	DPM3 LC2	DPM3 LC1	
		LC4	LC3	
		LC6	LC5	
		LC8	LC7	
		RDCC1	RDCC0	
			(HUBC)	
ļ		DKU3,4	DKU1,2	
		PSU0,1	DKU5,6	

FIG. 3B

Add	b7~b4	b3~b0	Remarks
0018	DPM4 LC2	DPM4 LC1	
000ъ	LC4	LC3	
	LC6	LC5	
000e 000f	LC8	LC7	
10001	RDCC1	RDCC0	
		(HUBC)	
	DKU3,4	DKU1,2	
	PSU0,1	DKU5,6	
0020 0021	CPM NIC2	CPM NIC1	DKU,PSU: PSB_TB
0022	NIC4	NIC3	conversion from PSB-TB to PSB
0023 0024 0025	EIOC1	EIOC0	
0026			
0027	ASCC1	ASCC0	
	NSPC3	NSPC2	
	RDCC1	RDCC0	
	PSU0,1	PSU1,2	

FIG. 4

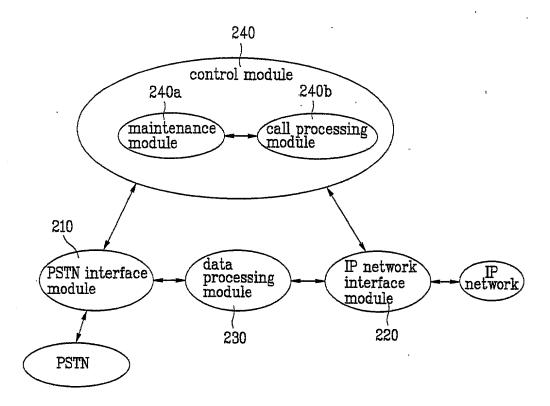


FIG. 5

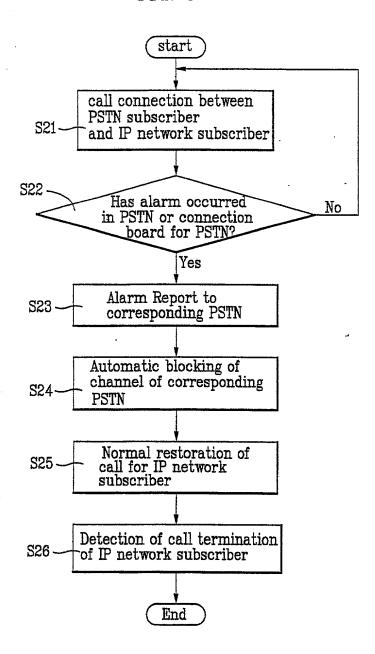


FIG. 6

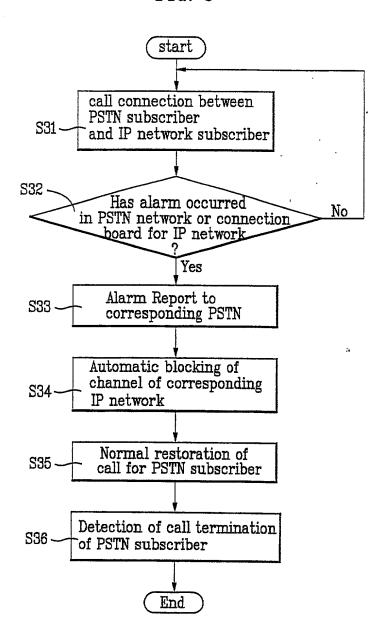


FIG. 7

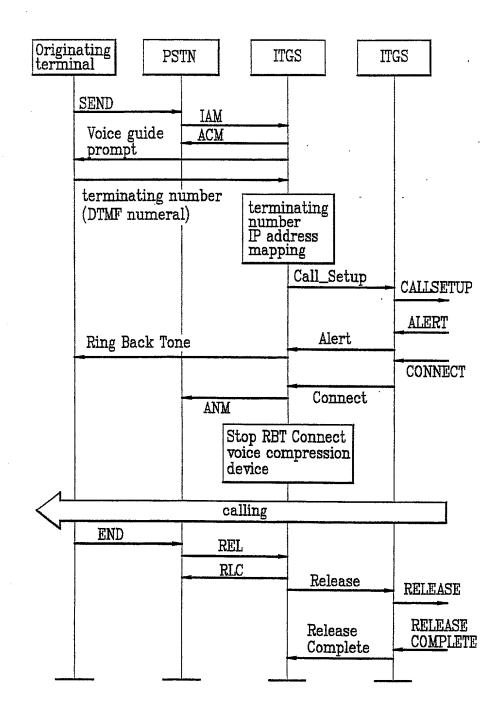


FIG. 8

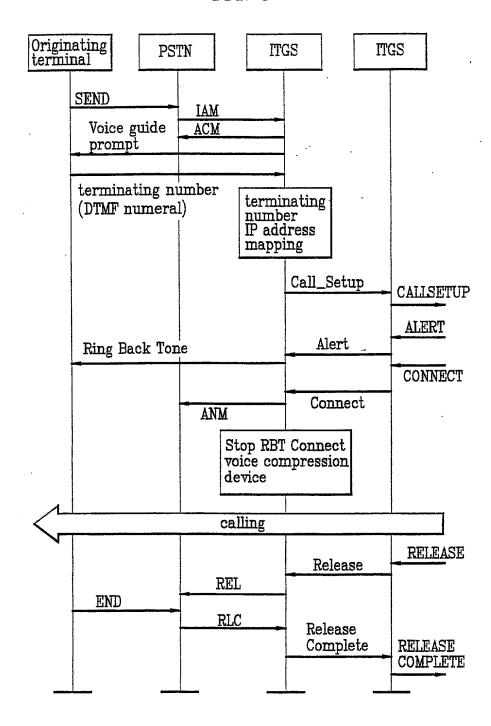


FIG. 9

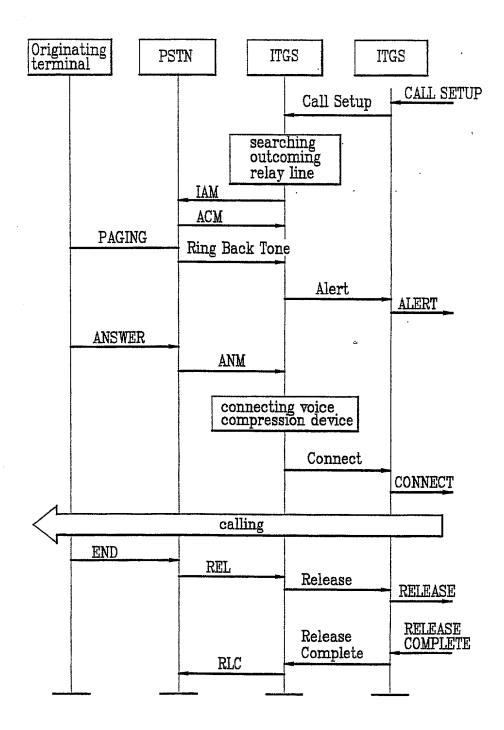


FIG. 10

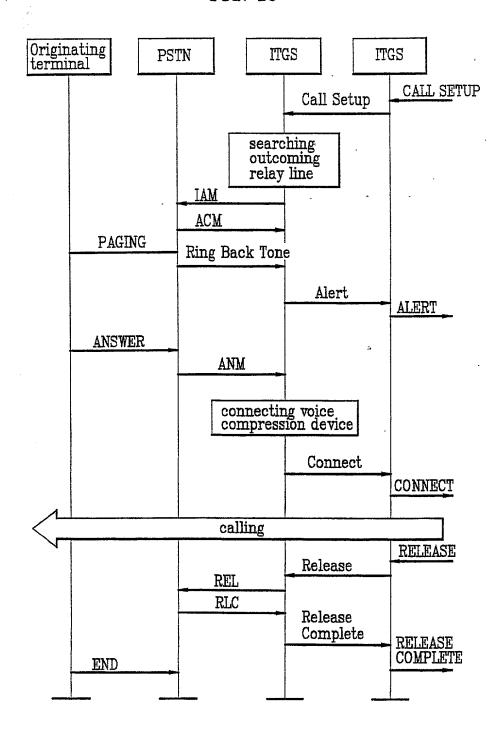


FIG. 11

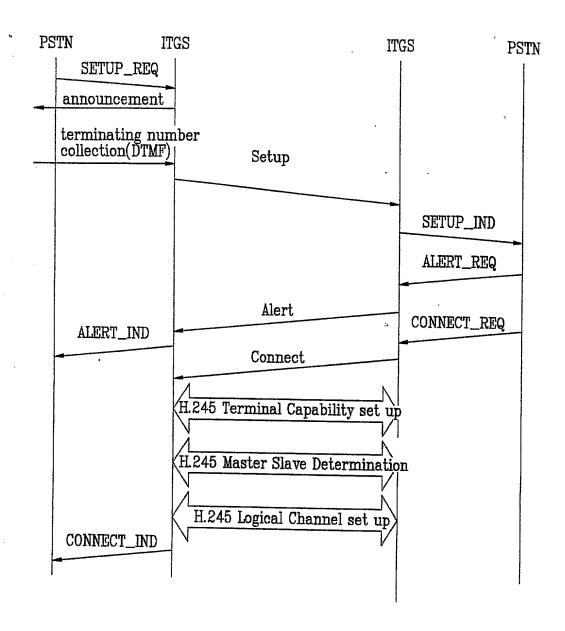


FIG. 12

